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# Accepted Manuscript

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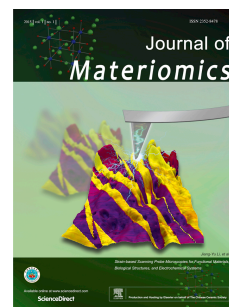
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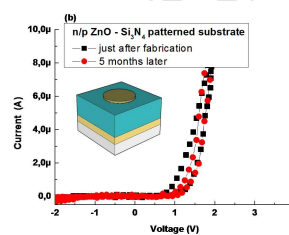
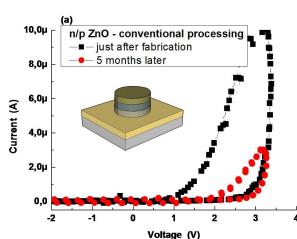
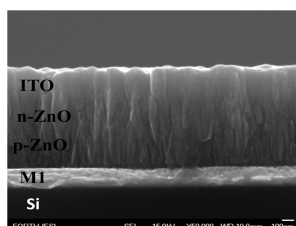
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## Graphical Abstract

We fabricated n/p ZnO homojunctions by sputtering from a single zinc nitride target on room temperature substrates. The bipolar ZnO:N layers were obtained by adjusting the  $O_2$  content of the  $O_2$ -Ar plasma. An all-oxide sputtered homodiode had transparency of around 85%. The n/p ZnO formed on patterned substrates and stored in atmosphere for five months revealed very stable diode characteristics.



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#### Highlights:

- ZnO homojunction fabricated by sputtering at room temperature
- Single zinc nitride sputtering target for p-ZnO and n-ZnO films
- Highly stable ZnO homojunctions formed on Si<sub>3</sub>N<sub>4</sub>-patterned substrates
- One step sputtering deposition for transparent all-oxide homojunctions

ZnO-based n/p homojunctions were fabricated by sputtering from a single zinc nitride target at room temperature on metal or ITO-coated glass and Si substrates. A multi-target rf-sputtering system was used for the growth of all oxide films as multilayers in a single growth run without breaking the vacuum in the growth chamber. The nitrogen-containing films (less than 1.5 at.% of nitrogen) were n-type ZnO when deposited in oxygen-deficient Ar plasma (10% O<sub>2</sub>) and p-type ZnO when deposited in oxygen-rich Ar plasma (50% O<sub>2</sub>). The all-oxide homojunction ITO/n-ZnO/p-ZnO/ITO/glass was fabricated in a single deposition run and exhibited visible transparency in the range of 75-85%. The n/p ZnO homojunctions, having metallic contacts, formed on conventionally processed substrates showed a fairly unstable behavior concerning the current-voltage characteristics. However, the same homojunctions formed on Si<sub>3</sub>N<sub>4</sub>-patterned substrates and stored in atmosphere for a period of five months were stable exhibiting a turn-on voltage of around 1.5 V. The realization of a room temperature sputtered transparent and stable ZnO homojunction paves the way to the realization of all-oxide transparent optoelectronic devices.

**Keywords:** ZnO homojunction, p-type ZnO, single step sputtering, stability, patterned substrate

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## 1. Introduction

Over the last decade the rapid development in optoelectronic devices like solar cells, sensors and light emitting diodes, combined with transparency and flexibility, has lead to applications in a variety of fields spanning from consumable portable and wearable electronic products [1,2], energy and environment [3,4] to bio- and nano-technology materials for human health [5]. Main requirements for these devices have been the realization of the vital component of solid state electronics namely the n/p junction, a reliable operation, the low temperature fabrication processes and the high performance at low manufacturing cost.

Zinc oxide (ZnO) is an extensively studied material for optoelectronic applications because it combines attractive physical properties (optical transparency, high exciton binding energy, high piezoelectricity and a low thermal expansion coefficient) with abundance and no hazardous or toxic characteristics. Nonetheless, ZnO is an intrinsically n-type material caused by excess Zn and oxygen deficient structure. Due to the inherent difficulty to form reliable and controllably stable p-type material (arising from self-compensation effects, deep acceptor levels and low solubility of acceptors) the use of ZnO has been restricted only either as a passive transparent conductive layer or as active n-type material in n/p heterostructures where the p-type element may be a different material such as GaN, Si, NiO, SiC, etc [6,7]. However, crystallographic mismatch, lattice misalignment and energy band discontinuities at the n-p interfaces of such heterostructures generally lead to electrical or optical active defects (traps) which degrade the output characteristics of the resulting device. To eliminate such drawbacks, including fabrication costs issues, attempts have been made towards a stable and controllable p-type ZnO for the fabrication of n/p ZnO homojunctions. Along these lines, p-type doping engineering has been reported for creating acceptor dopants in ZnO, such as excess oxygen in the film during deposition [8], single doping from group-I (Ag, Li, Na, K) or group-V (N, P, As, Sb) elements and dual doping (P-N, Li-N, Ag-N, etc.) or co-doping with donor and acceptor (Ag-N, Al-N, In-N, etc.) [7]. Nonetheless, due to similarities of electronegativity and ionic radius between nitrogen and oxygen, nitrogen is the most attractive and commonly used dopant for p-type ZnO. Thus, n/p or p/n ZnO homojunctions, either as thin films or as nano-rods, have been developed by sputtering [8-12], laser assisted molecular beam epitaxy [13], chemical vapor deposition [14,15], pulsed laser deposition [16-18], sol gel [19]), spray pyrolysis [20-23] and atomic layer deposition [24]. The majority of these techniques utilize high temperatures ( $>300\text{ }^{\circ}\text{C}$ ) either during deposition to improve the quality of the structure or/and after deposition to improve the ohmic contacts or activate the n/p ZnO homojunction. This, however, creates major technical limitations if these devices are to be applied for emerging portable, bendable or wearable applications. Until today fabrication of n/p ZnO at room temperature has been realized only by plasma containing deposition techniques, namely sputtering [25] or plasma assisted molecular beam epitaxy [26]. Irrespective of the method of fabrication, a number of n/p ZnO homojunctions issues concerning the long term stability and the rectification properties of the homo-diode have been reported and attempts have been

made to overcome them. For example, deterioration of the junction due to contamination from the ambient atmosphere has been investigated by fabricating the diode in both p/n and n/p configurations [24], unstable defects were eliminated by modifying deposition conditions [18] whereas inter-diffusion of nitrogen at the interface [14] has been tackled by inserting an ultrathin  $\text{Al}_2\text{O}_3$  layer at the n-p interface [24].

In a previous investigation we had shown that nitrogen-containing ZnO layers, of both n- and p-type conductivity, could be fabricated at room temperature from a single zinc nitride sputtering target by adjusting the oxygen content in the Ar plasma during deposition [27]. In this way, it was possible to realize an n-ZnN/p-ZnO heterostructure by fabricating all layers in a single growth run without chamber vent. In this work we present the fabrication of an n/p ZnO homojunction structure from a single sputtering target at room temperature on unpatterned and  $\text{Si}_3\text{N}_4$ -patterned substrates along with an investigation of the long term behavior. The realization of an n/p ZnO on a patterned substrate at room temperature is a novel approach for fabricating all-oxide transparent n/p ZnO homojunctions with improved stability for transparent as well as on-chip integration applications.

## 2. Experimental Details

### 2.1 Thin films deposition

The thin films used in this investigation, zinc oxide (ZnO) and indium tin oxide (ITO), were deposited by employing the rf (radio frequency) sputtering technique using a multi-target Nordiko NS2500 system equipped with rotatable substrate holder. The multilayer structures reported in this paper were deposited in a single deposition run without venting the chamber, thus eliminating possible contamination of the layers' interface. The mixture of the gases (i.e. Ar and  $\text{O}_2$ ) was controlled by varying their flows, through calibrated mass flow controllers, so that the total pressure in the chamber was kept constant. Prior to each deposition run, the sputtering chamber was pumped down to a base pressure better than  $1 \times 10^{-6}$  mbar ( $1 \times 10^{-4}$  Pa). The target-substrate distance was 10 cm and UV-grade fused silica and pieces of Si (100) wafer were used as substrates. The native oxide on the Si substrates was removed in diluted HF solution and all substrates were cleaned in organic solvents (acetone and 2-propanol), thoroughly rinsed in 18 M $\Omega$  de-ionized water and blow-dried using  $\text{N}_2$  flow.

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The zinc oxide thin films were deposited from a 6-inch diameter zinc nitride target. The rf-power used was 100W, the total pressure was 5 mTorr (0.67 Pa) and 10% O<sub>2</sub> or 50% O<sub>2</sub> in Ar plasma was used for growing n-type ZnO and p-type ZnO films, respectively. Details of the deposition conditions of ZnO films can be found elsewhere [27]. Before each ZnO deposition run the target was pre-cleaned initially in Ar plasma for 10 minutes followed by N<sub>2</sub> plasma for 60 minutes in an attempt to remove any possible contaminants for the surface of the target, keep its stoichiometry and bring it in equilibrium conditions.

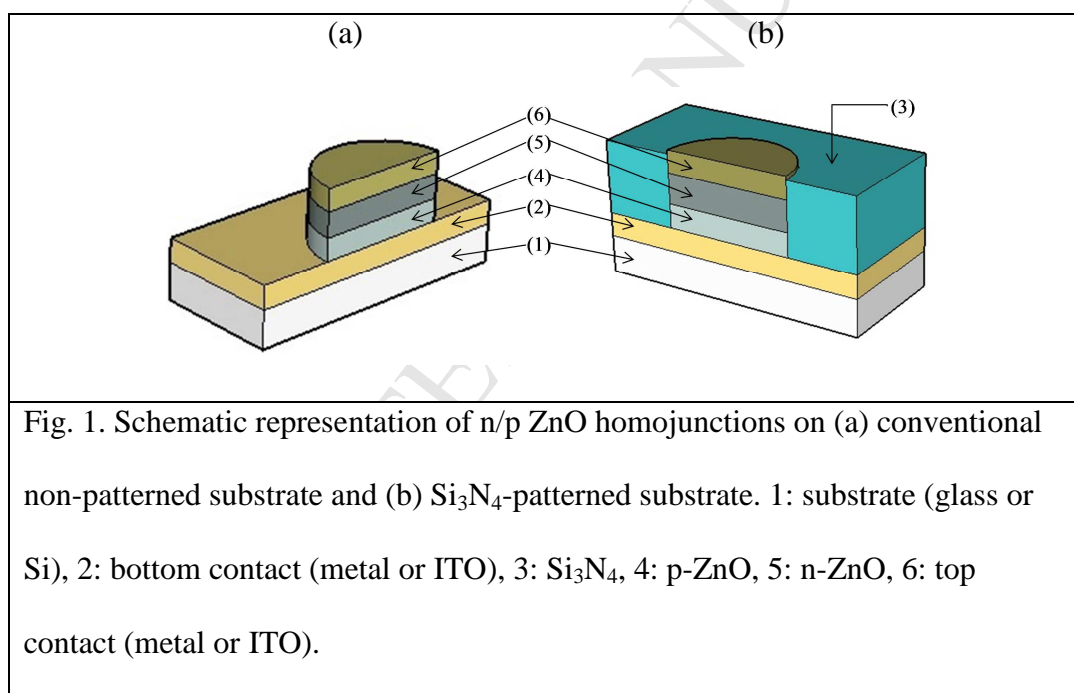
Indium tin oxide (ITO) was used in order to test the transparency of the ZnO homojunctions in the ITO/n-ZnO/p-ZnO/ITO/glass configuration. The ITO thin films were deposited from an indium-tin-oxide target (80% In<sub>2</sub>O<sub>3</sub>–20% SnO<sub>2</sub>) in pure Ar, at 5 mTorr (0.67 Pa) pressure and 300 W rf-power. The ITO-coated substrates were annealed in a Rapid Thermal Annealing system (JIPLEC FAV4) for 1 minute at 400 °C in N<sub>2</sub> atmosphere (450 sccm flow rate) in order to improve the electrical and structural properties of the ITO coating prior the ZnO deposition [28]. The nominal thickness of the ZnO and ITO thin films was around 170 nm unless otherwise indicated.

## 2.2 ZnO homojunction fabrication

The substrates used for the fabrication of ZnO homojunctions were ITO-coated or Ag/Au-coated glass and Si pieces. The latter metallization, deposited by electron beam evaporation, was used as the ohmic contact to the p-ZnO layer which would be the first layer to be sputter-deposited for the formation of the diode. By exploiting the fact that the ZnO layers of the diode could be formed on unintentionally heated substrates, the sputtering chamber was loaded with the above mentioned substrates which had been patterned with photoresist, having dot geometry with diameter of 700 μm, employing standard optical lithography and lift-off techniques. In addition, the dot geometry patterning was also made on Si<sub>3</sub>N<sub>4</sub>-coated substrates. For this case, the formation of the dot geometry was transferred from Si<sub>3</sub>N<sub>4</sub> down to the substrate by employing reactive ion etching (RIE). After the p-ZnO and n-ZnO layers deposition, as described above, the samples were removed from the sputtering chamber and the top ohmic contact (Ti/Au) for the n-ZnO was formed by electron beam evaporation followed by lift-off. Alternatively, after the ZnO depositions and without venting the chamber, the sample holder was moved over the ITO target for the deposition of the



ITO layer which was followed by the lift-off procedure. In this way, two different diode configurations could be formed in the same growth run, one on conventional substrate and another one on  $\text{Si}_3\text{N}_4$ -patterned substrates. The cross-sectional schematic representations of the diode configurations can be seen in Fig.1 where the numbering of the layers gives also the sequence of the processing steps used for the fabrication of the diode. The conventional n/p ZnO diode configuration which was unprotected from the ambient atmosphere is presented in Fig.1(a), whereas the  $\text{Si}_3\text{N}_4$ -protected n/p ZnO configuration which, throughout its fabrication, was not exposed to the ambient is seen in Fig.1(b). The patterned  $\text{Si}_3\text{N}_4$  layer, which played the role of protective layer for the ZnO homojunction, was formed by Plasma Enhanced Chemical Vapour Deposition (PECVD –Vacutec 1250) in  $\text{SiH}_4:\text{NH}_3:\text{N}_2$  plasma at  $300^\circ\text{C}$ , had a thickness of around 350 nm and its etching was made in a RIE system (Vacutec 1250) in fluorine containing plasma.



### 2.3 Thin films and device characterization

The structural properties of the films were examined by X-ray diffraction (XRD) and Transmission Electron Microscopy (TEM). XRD measurements were performed using a Rigaku D-max 2000 system, with  $\text{Cu K}\alpha 1$  radiation ( $\lambda=1.5405\text{\AA}$ ). The films were scanned in a Bragg-Brentano configuration ( $\theta$ - $2\theta$ ) from  $30^\circ$  to  $90^\circ$ . The surface or the cross-sectional morphologies of the layers were investigated by Field-Emission

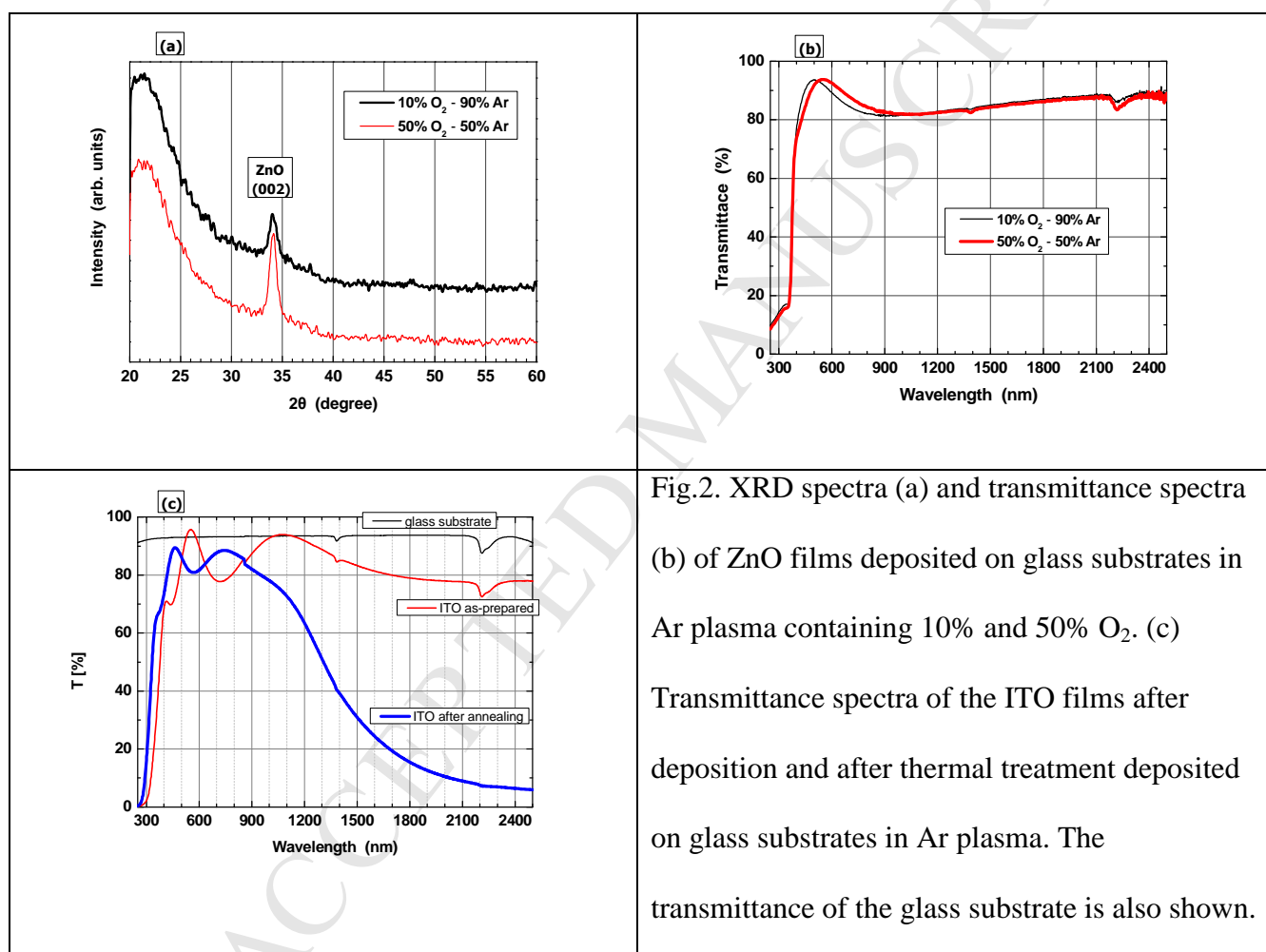
Scanning Electron Microscope (FE-SEM, Jeol 7000F), operating at 15 keV whereas high resolution cross-sectional Transmission Electron Microscopy (XTEM) imaging was carried out using a JEOL 2100 HRTEM operated at 200 kV in Bright Field (BF) mode and Selective Area Electron Diffraction (SAED) mode using a Gatan Double Tilt holder. The XTEM analysis was made on samples prepared by the Focus Ion Beam technique. The cross-section of the samples was obtained by employing FEI's Dual Beam Helios Nanolab 600i system using Ga ion beam and two layers of protective material were used, namely electron beam Pt and ion beam C. The lamellas were thinned and polished at 30 kV & 100 pA and 5 kV & 47 pA, respectively. Atomic force microscopy (AFM) measurements were performed in order to examine the surface morphology and roughness of the films. A Digital Instrument-Multimode system was employed, with maximum scanning area  $100\mu\text{m}\times 100\mu\text{m}\times 5.6\mu\text{m}$ , working in the Tapping Mode using Si-sharp tips (radius of curvature 10-15 nm).

The transport properties of the films (resistivity, carrier concentration and Hall mobility) were determined by Hall-effect measurements using the four-probe Van der Pauw technique and the optical properties of the films were examined by recording the transmittance in the UV-Vis-NIR spectrum (Perkin Elmer Lambda 950 spectrophotometer). The transmittance (T) values were used to get an estimation of the optical direct band gap,  $E_g$ , of the films through the standard relations  $T=\exp(-\alpha d)$ ,  $(\alpha h\nu)^2=A(h\nu-E_g)$  and the Tauc plot, where  $\alpha$  is the absorption coefficient,  $d$  is the thickness,  $h$  is the Planck's constant and  $\nu$  is the photon's frequency. All measurements were performed on as-prepared films unless it is stated otherwise and the thickness was determined by using a stylus profilometer (Veeco Dektat 150). The current-voltage (I-V) characteristics of the homodiodes were recorded using a programmable curve tracer (Sony, Tektronix 370).

### 3. Results and discussion

#### 3.1 Thin films Properties

In previous experiments [27, 29] it had been shown that films sputtered from a zinc nitride target in plasma containing a mixture of Ar-O<sub>2</sub> gases could produce n-ZnO films in oxygen-deficient plasma (less than 30% O<sub>2</sub>) and p-ZnO in oxygen-rich plasma (more than 40% O<sub>2</sub>). Even though the existence of nitrogen in the films could not be detected unambiguously by EDX since its signal was at the detection limit of the system, the n-type conduction (oxygen-deficient plasma) was attributed to donors formed by interstitial zinc and oxygen vacancies, whereas the p-type conduction (oxygen-rich plasma) was due to acceptors formed by zinc vacancies, oxygen interstitial and N-on-O substitutions [6, 27, 30-32].



The structural properties of the individual n-ZnO and p-ZnO layers which constituted the n/p ZnO homojunction are seen in Fig.2(a) where the XRD patterns of the films deposited in 10% O<sub>2</sub> and 50% O<sub>2</sub> in Ar plasma are depicted. Both films had the hexagonal wurtzite structure of ZnO with the (002) diffraction peaks located at 34.03° and 34.14° for the films made in low oxygen plasma (10% O<sub>2</sub>) and rich oxygen

plasma (50% O<sub>2</sub>), respectively. The lower diffraction angle of (002) ZnO peaks than that given by the Joint Committee of Powder Diffraction System (JCPDS) card 36-1451 (34.42°) can be attributed to the low temperature fabrication of the films and the associated poor crystallinity of the films [33]. Furthermore, the difference in the (002) diffraction angle between the two films can be attributed to the built-in stress in the structure of the films due to the existence of nitrogen and the different amounts of oxygen in the Zn-O structures, and the differences on the atomic radius of N (0.56 Å) with those of O (0.48 Å) and Zn (1.42 Å) [10-12, 29]. The transmittance of the films is presented in Fig.2(b). Both films have a transparency of ~80-90% in the visible region of the spectrum. The energy band gap of the films, extracted from Tauc plot, was 3.31 eV for the film deposited in 10% O<sub>2</sub> in plasma and 3.35 eV for the 50% O<sub>2</sub> film, indicating an increase of the optical band gap with the O concentration in the layers. The increased transmittance below the absorption edge seen in Fig.2(b) is a feature generally observed either for undoped or doped ZnO films [33-35]. Table 1 presents the electrical properties of the N-doped ZnO films where it is seen that oxygen-rich plasma (50% O<sub>2</sub>) leads to p-type ZnO films but with lower carrier concentration and mobility and higher resistivity than those of n-ZnO films grown in an oxygen-deficient plasma (10% O<sub>2</sub>).

The n-type ITO films were used either as transparent and conductive coatings on the substrates and/or as cap-protective layer for the ZnO homojunction. Since it is well established that the properties of any polycrystalline thin film can be improved by post-deposition annealing, ITO-coated substrates had been annealed prior to any further use so to improve their properties [28] whereas ITO films which were deposited followed the depositions of ZnO layers did not undergo any thermal treatment. The surface of the ITO films exhibited an rms roughness of around 2.7 nm which was slightly reduced to 2.3 nm after annealing as extracted from AFM observations (not shown here). The optical and electrical behaviour of the as-prepared ITO films and their improvements after thermal treatment are presented in Fig.2(c) and Table 1, respectively. Annealing of ITO improved crystallinity, healed defects, traps and structural imperfections leading to improved electrical characteristics like reduced resistivity, increased carrier concentration and a small increase of carriers' mobility. The improvement in the electrical properties of the ITO film after annealing was reflected in the reduced infra-red transmittance presented in Fig.2(c) due to increased carrier concentration after annealing and thus increased carrier absorption. Similarly, the shift of the absorption

edge to shorter wavelengths and thus increase of the energy band gap with annealing was due to the increase of carrier concentration and the Moss-Burstein effect [36].

TABLE 1. Electrical properties of rf sputtered ZnO:N and ITO films on room temperature glass substrates.

Thin film layers (plasma conditions)	Treatment	Sheet Resistance ( $\Omega/\text{sq.}$ )	Resistivity ( $\Omega\cdot\text{cm}$ )	Carrier Concentration ( $\text{cm}^{-3}$ )	Mobility ( $\text{cm}^2/\text{Vs}$ )	Type
<b>ZnO:N</b> (10% O <sub>2</sub> -90% Ar)	As-prepared	$9.7 \times 10^4$	$3.6 \times 10^0$	$-1.6 \times 10^{18}$	$7.8 \times 10^0$	n
<b>ZnO:N</b> (50% O <sub>2</sub> -50% Ar)	As-prepared	$6.8 \times 10^5$	$3.2 \times 10^1$	$+4.2 \times 10^{17}$	$6.1 \times 10^{-2}$	p
<b>ITO</b> (100% Ar)	As-prepared	$4.7 \times 10^2$	$1.2 \times 10^{-2}$	$-5.9 \times 10^{19}$	$8.9 \times 10^0$	n
	Annealed	$9.3 \times 10^1$	$2.3 \times 10^{-3}$	$-3.0 \times 10^{20}$	$9.5 \times 10^0$	n

In the next section the properties of the n/p ZnO homojunctions are presented where the diodes have the configuration: M2/n-ZnO/p-ZnO/M1/substrate, where the substrate can be glass or Si and M1, M2 can be metals or ITO. It is worth reminding that with the existing sputtering system and by performing the deposition on unintentionally heated substrates, the n-ZnO, p-ZnO and M2 layers could be deposited in a single deposition run without system venting and thus without exposing individual layers and interfaces to the detrimental effects of atmosphere exposure.

### 3.2 Characterization of the n/p ZnO diode

As shown in the previous section, all oxide layers fabricated in this investigation, n-ZnO, p-ZnO and ITO, were transparent and all of them could be grown on top of each other in a single growth run. The transparency of n/p ZnO homojunction was tested by fabricating the diode having the configuration ITO/n-ZnO/p-ZnO/ITO/glass and total thickness  $d=800$  nm. Fig.3(a) shows the recorded transmittance (T) of the

above multilayers. The n/p ZnO homojunction sandwiched between two ITO layers and deposited on glass substrate had a visible transparency ( $400\text{ nm} < \lambda < 700\text{ nm}$ ) of around 75-85%, it remained transparent at around 85% until the near infrared ( $700\text{ nm} < \lambda < 1200\text{ nm}$ ) but the transmittance reduced for longer wavelengths due to increased absorption by carriers of the ITO-coated glass substrate (Fig.2(c)).

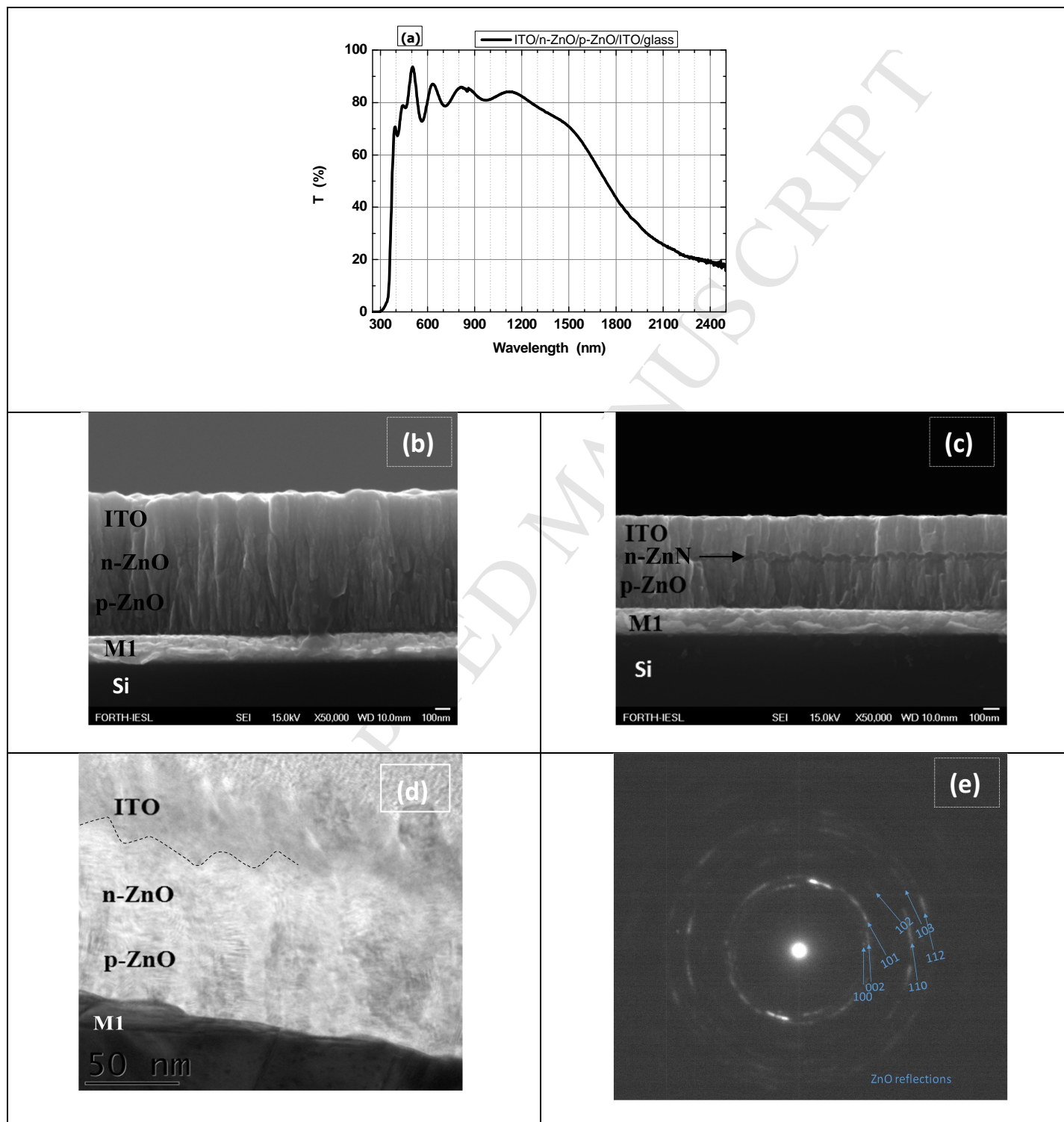


Fig.3. (a) Transmittance of an n/p ZnO diode having the ITO/n-ZnO/p-ZnO/ITO/glass configuration, (b) cross-sectional SEM image of ITO/n-ZnO/p-ZnO/M1/Si diode, (c) cross-sectional SEM image of ITO/n-

To investigate the morphology of the structure by SEM and avoiding any charging effects, the ITO/glass substrate was replaced by metal-coated/Si (M1/Si) substrate and cross-sectional SEM image of the n/p ZnO homojunction having the ITO/n-ZnO/p-ZnO/M1/Si configuration is seen in Fig.3(b). The structure of ZnO layers was columnar, oriented perpendicular to the substrate. No interface can be distinguished between the p-ZnO and n-ZnO layers implying successful elimination of defects and extra states at the interface, whereas a clear interface can also be seen between the n-ZnO and ITO layers having different crystallographic phases (hexagonal n-ZnO and cubic ITO). For comparison reasons, the n-ZnO was replaced by a thin (around 50 nm) n-ZnN layer, made in 100% Ar plasma and having the cubic crystallographic phase [34], while keeping the deposition conditions of the p-ZnO layer the same like those of Fig.3(b). The three distinct layers, p-ZnO, n-ZnN and ITO, can be clearly seen in the cross-sectional SEM images of Fig.3(c). The columnar structure of a n/p ZnO homojunction, having a diode thickness of around 130 nm, was examined by TEM and the results, XTEM image as well as spot reflections in SAED patterns, are depicted in Fig.3(d) and (e). The growth mode of ZnO's grains and the lack of any interface imperfections between the p-ZnO and n-ZnO layers confirmed the observations made by the SEM images (Fig.3(b)) that the n/p ZnO homojunction behaves crystallographically as a single phase wurtzite ZnO structure. This implies no discontinuities or states at the n-ZnO/p-ZnO interface indicating that any deterioration of the homojunction characteristics with time would arise mainly from the effects of its exposure to the atmosphere. The dash line has been drawn at the ZnO-ITO interface as a guide to the eye.

The electrical behavior of the n/p ZnO homojunction was tested after processing the structure as a diode having dot geometry employing the conventional substrate as well as the Si<sub>3</sub>N<sub>4</sub>-patterned substrate, as described in the Experimental Details section and illustrated in Fig.1. After carefully examining and solving various processing issues encountered during diode fabrication the optimized patterned substrates were



loaded into the sputtering chamber. The n/p ZnO diodes were grown on metal-coated glass substrates having the configuration M2/n-ZnO/p-ZnO/M1/glass where M1 was a Ag/Au metallization used as bottom contact to p-ZnO (Ag is acceptor for ZnO [7, 37]) and M2 the Ti/Au top contact to n-ZnO. It is worth mentioning that the formation of ohmic contacts for p-ZnO and consequently for n/p ZnO homojunction is an open technological challenge not only because metals with high work functions are required but at the same time the difficulties in obtaining reliable p-ZnO layer and the associated stability issues of the layer have to be satisfied. It is not surprising that various metallization types have been reported in the literature, where single metals, bimetals or multilayer schemes have been applied as contacts to the p-ZnO and n-ZnO layers. Table 2 lists the metallization schemes for ZnO homojunctions reported over the last decade. In the present work, in order to avoid introducing any artifacts due to stability issues of the p-ZnO layer no tests were performed on the ohmic behavior of Ag/Au on p-ZnO. The Ti/Au metallization was used as ohmic contact for n-ZnO since it has been proven to be ohmic contact on n-ZnN layers (contact resistance  $1.9 \times 10^{-4} \Omega \text{cm}^2$  [27]).

TABLE 2. Reported metallization schemes, over the last decade, as contacts for ZnO homojunctions.

<b>n-ZnO ohmic contact</b>	<b>p-ZnO ohmic contact</b>	<b>[Ref.] Year</b>
Au	Au	[11] 2017, [17] 2013
In	In	[21] 2014, [20] 2013, [19] 2012, [9] 2011
Pt	Pt	[15] 2017
silver-paste	silver-paste	[10] 2012
In	Ni/Au	[12] 2013
In/Au	In/Au	[14] 2008
In/Zn	In/Zn	[16] 2008
Zn:Al	Zn:Al	[25] 2013
Ti/Au	Ti/Au	[24] 2015
Ti/Au	Ag/Au	<b>Present Work</b>
Ti/Au	ITO	[38] 2011
ITO-ZnO	Ni/Au	[39] 2015



Fig.4 shows the current–voltage (I–V) characteristics of the n/p ZnO homojunction diodes fabricated by the two different patterned substrates approach, seen in the inset, just after fabrication as well as after a period of five (5) months where the diodes had been left exposed to the ambient atmosphere. It was found that the I-V characteristics of the diodes showed rectifying behavior but the Si<sub>3</sub>N<sub>4</sub>-patterned diode (Fig.4(b)) showed much more stable characteristics than the ‘conventional’ and unprotected diode (Fig.4(a)). It should be reminded that the diode made on the Si<sub>3</sub>N<sub>4</sub>-patterned substrate was exposed to the ambient atmosphere only during deposition of its top contact and during its 5 months exposure to the ambient was protected by the Si<sub>3</sub>N<sub>4</sub> layer (see Fig.1(b) and inset in Fig.4(b)). The measured low current for the applied voltage might be due to high resistance of metal-semiconductor contacts, as mentioned above and the I-V hysteresis is associated with the trapping and detrapping of carriers in deep states within the structure during the I-V measurement. A turn on voltage (V<sub>t</sub>) of around 1-1.5 V can be seen in Fig.4(b) for the stable Si<sub>3</sub>N<sub>4</sub>-patterned homojunction. The build-in potential V<sub>D</sub> of the diode was calculated from the equation [40]:

$$V_D = \frac{k_B T}{q} \left[ \ln \frac{N_D N_A}{n_i^2} \right]$$

where  $k_B T/q$  is the thermal voltage (25.58 meV),  $k_B$  is the Boltzmann constant,  $T$  is the room temperature 300 K,  $q$  is the electron charge,  $N_D$  and  $N_A$  are the donor concentration of n-ZnO ( $1.6 \times 10^{18} \text{ cm}^{-3}$ ) and acceptor concentration of p-ZnO ( $4.2 \times 10^{17} \text{ cm}^{-3}$ ), respectively (see Table 1), and  $n_i$  is the intrinsic carrier concentration of ZnO ( $\sim 1 \times 10^6 \text{ cm}^{-3}$  [41]). The  $V_D$  value was found to be 1.20 eV which is in agreement with the turn-on voltage  $V_t$  of 1-1.5 V seen in the I-V curve of Fig.4(b). Most of the reported values of  $V_t$  for ZnO homojunctions are generally observed between 1 and 3 V and differences between the values of  $V_D$  and  $V_t$  have been attributed to an increased resistance due to defects at the n-ZnO and p-ZnO interface introduced during the growth process [18]. Such imperfections at the interface have been eliminated in the n/p ZnO homojunction of this work where the transition from the p-ZnO to n-ZnO was done in a non-stop growth process. Stable ZnO homojunctions with low turn-on voltage values are two of the critical parameters for use in device applications. Additional work is needed to analyze further the I-V characteristics of the diode and tailor its properties, by applying the appropriate engineering procedures, in an effort to enhance output characteristics. Furthermore, it should be noted that the characterized n/p ZnO

devices shown in Fig.4 are not transparent since metallic contacts were used. However, as shown in the above sections, the presented results are very promising towards the fabrication of stable transparent all-oxide ZnO homodiodes in a rather facile way by applying the appropriate deposition and processing procedures. Moreover, the use of room temperature fabrication technique may promote them as potential candidates for flexible and wearable optoelectronic applications.

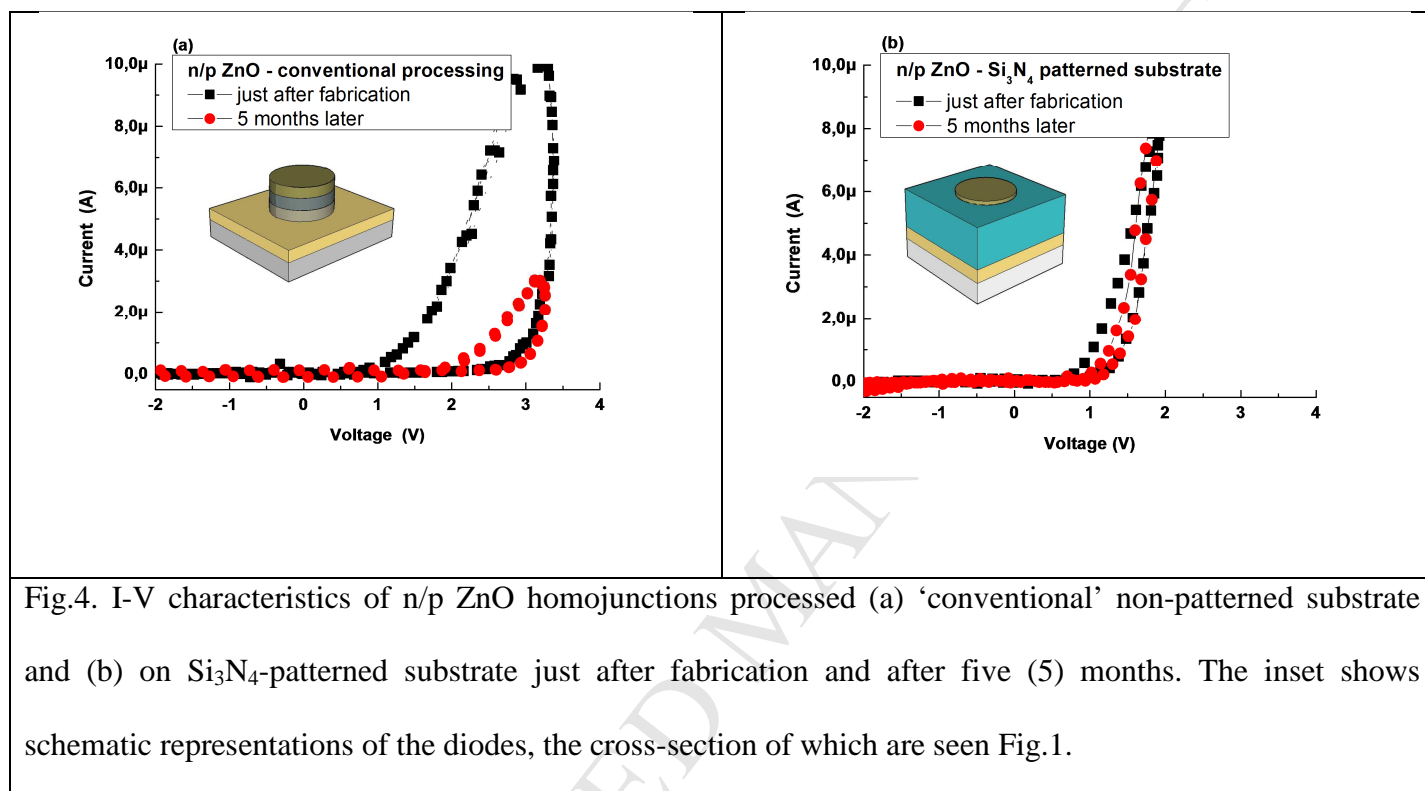


Fig.4. I-V characteristics of n/p ZnO homojunctions processed (a) ‘conventional’ non-patterned substrate and (b) on Si<sub>3</sub>N<sub>4</sub>-patterned substrate just after fabrication and after five (5) months. The inset shows schematic representations of the diodes, the cross-section of which are seen Fig.1.

## Conclusions

Concluding, a zinc nitride sputtering target was used to fabricate in a single growth run and at room temperature transparent and stable n/p ZnO homojunction thin film structures. The bipolar N-containing ZnO layers were successively deposited by adjusting the O<sub>2</sub> content of the O<sub>2</sub>-Ar plasma through mass flow controllers for transforming from the n-type ZnO to p-type ZnO layers. N-type ZnO was formed in low-O<sub>2</sub> plasma (10% O<sub>2</sub>) whereas O<sub>2</sub>-rich plasma formed p-type ZnO layer. The transparency of the homojunction was tested having the all-oxide configuration ITO/n-ZnO/p-ZnO/ITO/glass, made without breaking sputtering vacuum during depositions thus eliminating any imperfections at the layers’ interfaces. The transmittance was found to be around 75-85% in the visible solar spectrum region. The stability of ZnO

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homojunction was examined by forming the n/p ZnO configuration metal/n-ZnO/p-ZnO/metal/Si on conventionally processed Si substrate and on Si substrate coated by a patterned Si<sub>3</sub>N<sub>4</sub> layer. The Si<sub>3</sub>N<sub>4</sub> layer played the role of protective layer for the ZnO homojunction as a result of which exposing the diodes to the ambient atmosphere for five months provided stable I-V characteristics when compared to the diode formed on conventional substrates. With a turn-on voltage of around 1.5 V and by further optimizing the layers properties as well as the processing procedure steps, ZnO homojunctions with enhanced diode characteristics and stability may be realized for flexible and wearable optoelectronic devices applications.

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There are not available at this moment biographies and photos of other authors